

<b>PERSONAL DETAILS</b>			
<b>Name</b>	Mrs. Ramya. K		
<b>Department</b>	Electronics & Communication Engg.		
<b>Designation</b>	Assistant Professor		
<b>Qualification</b>	M.Tech.		
<b>Teaching Experience</b>	07 Years	<b>DOJ</b>	
<b>Email</b>	ramyayadav512@gmail.com	<b>Vidwan-ID</b>	119836
<b>Type of Association</b>	Regular		
<b>Core Discipline</b>	Electronics & Communication Engineering		
<b>Specialization</b>	Digital communication		
<b>Areas of Interest</b>	<ul style="list-style-type: none"> <li>· Communication</li> <li>· Networking</li> <li>· Image processing</li> <li>· VLSI</li> </ul>		
<b>MEMBERSHIP OF PROFESSIONAL BODIES</b>			
<b>Professional Society</b>	<b>From Year</b>	<b>Membership Number/ Nature</b>	
IEEE	March 2020	96799119/1 year	
AMIE	July 2016	AM-1639539/LIFETIME	
ISTE	JUNE 2020	LM131726	

## **PUBLICATIONS**

### **INTERNATIONAL JOURNALS: 15**

1. Ramya K “An Unattended Cluster based VANET-oriented Evolving Graph model and ATED Reliable Routing Theme” International Journal of Engineering Research and Technology ISSN:2278-0181 VOL-9 ISSUE-4 APRIL-2020
2. Ramya K “Performance and analysis of synchronous voltage mode buck converter using 180nm” International research journal of engineering and technology ISSN:2395-0056 VOL-07 ISSUE-6 JUNE-2020
3. Ramya K “Serial Out bit level Mastrovito Multiplier for High speed Hybrid double multiplication architectures” International research journal of engineering and technology ISSN:2395-0056 P-ISSN:2395-0072 VOL-4 ISSUE-5 MAY-2017
4. Ramya K “ VLSI Design for convolutive blind source separation” International research journal of engineering and technology ISSN:2395-0056 P-

ISSN:2395-0072 VOL-4 ISSUE-5 MAY-2017

5. Ramya.K participated in “THE FIRST ALL INDIA PAPER WRITING COMPETITION ON EMERGING RESEARCH(PACER 2020).
6. Ramya.K “Multitasking device to track Covid-19 Symptoms and emergency caller” International Advanced Research Journal in science Engineering and Technology” Volume 8, Issue 7 July 2021.
7. Ramya.K “A Design implementation and Comparative Analysis of Advanced Encryption Standard (AES) Algorithm using Verilog HDL

### **NATIONAL CONFERENCES:**

1. Ramya. K “An efficient VLSI implementation of DWT and DT-CWT for satellite images” KSHITIJA-13 on MAY 7, 2013, SJCIT, CHICKBALAPURA.
2. Ramya. K “Efficient medical Image enhancement of medical and satellite images using DT-CWT”

**WORKSHOPS/SEMINARS/FDPs/ PARTICIPATED:**

1. One week Faculty Development Program on “RECENT ADVANCES IN RF AND WIRELESS COMMUNICATION” During 2<sup>nd</sup> to 7<sup>th</sup> August 2021

2. AICTE Training And Learning (ATAL) Academy **Online Elementary FDP** on "**Modern Techniques for Wireless Communication** from **2021-05-17** to **2021-05-21** at **Madhav Institute of Technology & Science, Gwalior.**

3. AICTE Training And Learning (ATAL) Academy **Online FDP** on "**Data Sciences**" from **2021-1-19** to **2021-1-23** at **GLOBAL ACADEMY OF TECHNOLOGY.**

4. AICTE Training And Learning (ATAL) Academy **Online FDP** on "**ARTIFICIAL INTELLIGENCE**" from **2021-1-19** to **2021-1-31** at **SRI JAYACHAMAREJENDRA COLLEGE OF ENGINEERING**

5. AICTE Training And Learning (ATAL) Academy **Online FDP** on "**PHOTONICS**" from **2021-2-1** to **2021-2-5** at **NATIONAL INSTITUTE OF TECHNOLOGY, KARNATAKA**

6. AICTE Training And Learning (ATAL) Academy **Online FDP** on "**Data Sciences**" from **2021-1-19** to **2021-1-23** at **GLOBAL ACADEMY OF TECHNOLOGY**

7. Two day FDP on “ENGINEERING PEDAGOGY” on 15<sup>th</sup> and 16<sup>th</sup> of DECEMBER-2015 in SJCIT, CHICKBALAPURA

8. Four-day FDP on “VIRTUAL INSTRUMENTATION ESSENTIALS FOR ACADEMIC AND RESEARCH IN ENGINEERING” 28<sup>th</sup> to 31<sup>st</sup> MAY- 2014 in AIT, CHICKMAGALUR

9. One day FDP ON “PRINCIPLES OF PEDAGOGY FOR EFFECTIVE INSTRUCTIONS DISSEMINATION” HELD ON 19 JULY 2014, SJCIT, CHICKBALAPURA