


**PERSONAL DETAILS**

<b>Name</b>	SRIVIDYA C N		
<b>Department</b>	Electronics & Communication Engg.		
<b>Designation</b>	Assistant Professor		
<b>Qualification</b>	M.Tech		
<b>Teaching Experience</b>	4 Years		
<b>Email ID</b>	srividyaen05@gmail.com		
<b>Vidwan-ID</b>	119892	<b>DOJ</b>	01/08/2017
<b>Orcid ID</b>	0000-0002-1035-7463	<b>Google Scholar ID</b>	
<b>Type of Association</b>	Regular		
<b>Core Discipline</b>	Electronics & Communication Engineering		
<b>Specialization</b>	VLSI Design and Embedded System		
<b>Areas of Interest</b>	<ul style="list-style-type: none"><li>· VLSI</li><li>· Image processing</li><li>· Communication</li><li>· Networking</li></ul>		

**MEMBERSHIP OF PROFESSIONAL BODIES**

<b>Professional Society</b>	<b>From Year</b>	<b>Membership Number/ Nature</b>
IEEE	April 2020	96774790/1 year

ISCA	August 2019	L-38478/ LIFETIME
AMIE	February 2016	AM-1914769/LIFETIME

## **PUBLICATIONS**

### **INTERNATIONAL JOURNALS: 3**

1. Srividya C N "Automatic Enemies Tracking and Color Changing Robot" Journal of Emerging Technologies and Innovative Research (JETIR), Volume 6, Issue 6 www.jetir.org (ISSN-2349-5162) July 2019.
2. Srividya C N "Autonomous All Time Medicine Counter for Medicine Self Dispensing" International Journal for Research in Applied Science & Engineering Technology (IJRASET), Volume 7 Issue V, May 2019, ISSN: 2321-9653.
3. Srividya C N "A Low Cost Automated Fluid Control System for Medical Application" International Journal of Advanced Engineering and Research Development(IJAERD), Volume 5 Issue 04, April-2018, e-ISSN:2348-4470, p-ISSN:2348-6406.

### **NATIONAL CONFERENCES: 4**

1. Srividya C N, Manojkumar S B " Design and Analysis of a Low-dropout regulator using 180nm CMOS Technology", Dept. of ECE, BGSIT, B G Nagar / 5<sup>th</sup> May 2017, Published in IJEBMS Volume 1, Issue 1, May 2017, Page no:464-467

2. Srividya C N " Automatic Enemies Tracking and Color Changing Robot", Dept. of ECE, NIE, Mysuru/16<sup>th</sup> and 17<sup>th</sup> May 2019, National Conference on Communication, Signal processing, IOT, Networking and Embedded Application (NCCOSINE-2019)

3. Srividya C N " Autonomous all time medicine counter for Medicine self-Dispensing", Dept. of ECE, NIE, Mysuru/16<sup>th</sup> and 17<sup>th</sup> May 2019, National Conference on Communication, Signal processing, IOT, Networking and Embedded Application (NCCOSINE-2019)

4. Srividya C N " Automated low-cost fluid control", Dept. of ECE, NIE, Mysuru/17<sup>th</sup> and 18<sup>th</sup> May 2018, National Conference on Communication, Signal processing, IOT, Networking and Embedded Application (NCCOSINE-2019)

### **WORKSHOPS/SEMINARS/FDPs/ PARTICIPATED: 9**

1. Eight days Workshop on "Python for data science", organized by Forsk Coding School from 18-25 April.

2. Three-day FDP on “Digital Era of Teaching” organized by Diksoochi, Mysore from 11 <sup>th</sup> May to 13 <sup>th</sup> May.		
3. One day FDP on “Management of intellectual property for IPRC”, by ACU Intellectual Property Rights Cell (ACUIPRC) and KSCST in association with CIPAM Govt. of India on 21 June 2019.		
4. Five-day FDP on “Machine Learning crash course (MLCC)” organized by Dept. of CSE and ISE, BGSIT, ACU and Google from 15 <sup>th</sup> December to 19 <sup>th</sup> December 2018.		
5. Four-day FDP on “Emerging Innovative approaches in VLSI, Communication & Network systems”, organized by Dept. of Electronics and Communication Engineering BGSIT, in association with KSCST, Govt. of Karnataka, from 10 <sup>th</sup> April 2018 to 13 <sup>th</sup> April 2018.		
6. Six-Day Faculty Development Program on “Embedded Controller Programming with ARM CORTEX-M3”, organized by Dept. of ECE, BGSIT, B G Nagar from 29 January to 3 February 2018		
7. Three-day FDP on “ARM Cortex M3” organized by Dept. of ECE, GSSSIETW, Mysore from 26 <sup>th</sup> to 28 <sup>th</sup> December 2017		
8. Seven-day Workshop on “Internet of Things” organized by Dept. of ECE, BGSIT, B G Nagar, Innovative Cell UMIC, IIT Bombay on 21-22 March 2017.		
9. Three-day workshop on “FPGA and VLSI System Dsign” organized by Dept. of ECE, PESCE, Mandya from 29 <sup>th</sup> Dec- 4 <sup>th</sup> Jan.		
<b>ONLINE COURSES</b>		
<b>Course Title</b>	<b>Course Organization</b>	<b>Year</b>
Digital Electronic circuits	NPTEL	2018-19
<b>AWARDS, COMMENDATIONS RECEIVED</b>		
<b>Nature</b>	<b>Year</b>	<b>Awarding Organization</b>
2 <sup>nd</sup> prize in project exhibition	2009	CPC polytechnic, Mysore.
GATE Qualified	2013	GATE

3 <sup>rd</sup> RANK in M.Tech	2015	PESCE, Mandya
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