PERSONAL DETAILS

Name	**Dr. Naveen K B**
Department	Electronics & Communication Engg.
Designation	Associate Professor
Qualification	M.Tech.,Ph.D.,MBA

Teaching Experience	11 Years

Email	naveenkb@bgsit.ac.in	**Scopus ID**	57218366953
Orchid ID	0000-0002-6103-7048	**Google Scholar ID**	bEfv9ssAAAAJ

DOJ	02-08-2010
Type of Association	Regular
Specialization	VLSI Design and Embedded System
Areas of Interest	· VLSI · Embedded Systems · Image processing · Communication · Networking

MEMBERSHIP OF PROFESSIONAL BODIES

Professional Society	From Year	Membership Number/ Nature
IEEE	2020	96718062

ISC	2019	L38398
IEAE(InSc)	2016	IEAE2017051
IEI	2016	AM163941
ISTE	2020	LM131718
PUBLICATIONS		
INTERNATIONAL JOURNALS:		
1. Naveen K B and Anusha et.al, "Diagnosis of Covid-19 from X-Ray using Machine Learning" International Advanced Research Journal in Science, Engineering and Technology, Vol.8, Issue 6, June 2021. 2. Naveen K B and Abhilash et.al, "Priority-based Ambulance-to-Traffic Light Communication for Delay Reduction in Emergency" Vol.8, Issue 6, June 2021.		

3. Naveen K B et.al, "Statistical Analysis of Attention Deficit Hyper Activity Disorder" Journal of HUazhong University of Science and Technology, ISSN: 1671-4512, Volume 50, Issue 02, Feb 2021.

4. Naveen K B and Dankan Gowda V et.al, "Interpretation of One Dimensional Hilbert Transform in terms of Fourier Transform and its Applications" International Journal of Advanced Science and Technology, Vol. 29, No.03, (2020), pp. 12283-12291.

5. Naveen K B and Dankan Gowda V et.al, "Internet of Things: Internet Revolution, Impact, Technology Road Map and Features", Advances in Mathematics: Scientific Journal 9 (2020) No. 7, 4405-4414, ISSN 1857-8365.

6. Naveen K B and Muddassira Begum et.al, " Accident Avoiding System using the IoT ", International Journal of Emerging Technologies and Innovative Research, ISSN 2349-5162, Vol. 6, Issue 5 , May 2019.

7. Naveen K B and Kushwanth R et.al, "IoT based Automatic Detection of Pot Holes and Speed Breaker Mapping", International Journal of Research in Engineering, Science and Management, ISSN: 2581-5792, Vol.-2, Issue-5, May-2019.

8. Naveen K B and Rajani et.al, "Monitoring of Ozone in Ambient Air and Controlling of Oxygen Generators with RTC" International Journal of Scientific Research and Review Volume 7, Issue 6, 2018 ISSN NO: 2279-543X, Volume 7, Issue 6, 2018, pp. 799-805.

9. Naveen K B and Naveen Kumar S K et.al, " Automatic Rain Water And Crop Saving

System", International Journal of Advance Engineering and Research Development Volume 5, Issue 05, May 2018.

10. Naveen K B and Anusha R et.al, "Video Call Based Home Security System Using Raspberry PI", International Journal of Advance Engineering and Research Development Volume 5, Issue 05, May -2018, pp-271-275.

11. Naveen K B and Shama Firdose., et.al, " Design And Implementation Of Android Based Automated Wheel Chair", International Journal of Engineering, Basic sciences, Management & Social studies Volume 1, Issue 1, May 2017.

12. Naveen K B and Dr. M.B Anandaraju., et.al, "Gigabit Passive Optical Network", International Journal of Engineering, Basic sciences, Management & Social studies Volume 1, Issue 1, May 2017.

13. Naveen K B and Nimitha K et.al, "Automatic Attendance System Using Webcam And RFID" International Journal of Engineering, Basic sciences, Management & Social studies Volume 1, Issue 1, May 2017, pp. 132-136.

14. Naveen K B and Kavya K, "Architecture of Static Random Access Memory Design Using 65nm Technology" International Journal of Engineering and Advanced Research Technology (IJEART) ISSN: 2454-9290, Volume-3, Issue-6, June 2017, pp. 6-8.

15. Naveen K B and Bharthi R et.al, "Design And Implementation Of Ripple Free, Short Circuit Protected Variable Power Supply For Technical Laboratories Using Multisim", International Journal of Engineering, Basic sciences, Management & Social studies Volume 1, Issue 1, May 2017, pp 457-463.

16. Naveen K B and Manoj Kumar S B et.al, " PIC and Place Human Follow Robot Using Ultrasonic Sensor", International Journal of Engineering, Basic sciences, Management & Social studies Volume 1, Issue 1, May 2017, pp. 312-320

17. Naveen K B and Kavya K, "Development of Automated Test System For RRF Duplexer", International Journal of Engineering, Basic sciences, Management & Social studies Volume 1, Issue 1, May 2017, pp 351-356.

18. Naveen K B and Divakar S, "RFID Based Smart Bus Tracking System Using Li-Fi Technology", International Journal of Engineering, Basic sciences, Management & Social studies Volume 1, Issue 1, May 2017, pp. 438-443.

19. Naveen K B and Dr. M N Sree Rangaraju et.al, " Energy Efficient QAM Modulation/Demodulation Architecture using Reversible Logic Gates" International Journal of Scientific & Engineering Research, Volume 7, Issue 8, August-2016, PP 759-763, ISSN 2229-5518.

20. Naveen K B and Divya K P et.al, "Energy Efficient Architecture for 4-TAP FIR Filter using Reversible Logic Gates", International Journal of Scientific & Engineering Research, Volume 7, Issue 7, July-2016, PP 645-752, ISSN 2229-5518

21. Naveen K B and Dr. M N Sree Rangaraju, "Low Power Digital ADC Architecture Using Reversible Logic Gatesin 65nm Technology", International Journal Of Electrical, Electronics And Data Communication, ISSN: 2320-2084 Volume-4, Issue-10, Oct.-2016, PP -104-109.

22. Naveen K B and Roopesh A R, et.al, "Approximate Low power, high-performance Adder

and Multiplier Design using Error Tolerance Application" International Journal of Engineering Research And Advanced Technology (IJERAT) ISSN: 2454-6135, Volume. 02 Issue.08, Aug – 2016.

23. Naveen K B and Yogananda C D, et.al, "Design of Convolutional Encoder Using 16 Bit Reversible Logic Vedic Multiplier" International Journal of Advances in Engineering Research (IJAER) 2016, Vol. No. 12, Issue No. II, August, pp. 19-29.

24. Naveen K B and Divya K P, "Energy Efficient QAM Modulation/Demodulation Architectures using Reversible Logic Gates", International Journal of Scientific and Engineering Researc h, Vol. 7, Issue 8, pp. 759-763, 2016.

25. Naveen K B and Dr. M. N Sree Rangaraju, "Energy Efficient Architectures for QPSK Modulation/ Demodulation Using Reversible Logic Gates in 65nm Technology," Research work communicated to Elsevier Miecroelectronics Journal, 2016.

26. Naveen K B and M N Sree Rangaraju, "Optimal Design of R2R DAC using Reversible Logic", International Journal of Advanced Research in Computer Science and Software Engineering, Volume 5, Issue 5, May 2015, pp 835-841.

27. Naveen K B and Anusha J K, et.al, "Design And Simulation Of Quadrature Phase Shift Keying Sub-System Using Reversible Logic" International Journal For Technological Research In Engineering Volume 1, Issue 10, June-2014, pp 1029-1033.

28. Naveen K B and Rashmi H et.al, " Design And Synthesis Of Binary LDPC Decoder" International Journal For Technological Research In Engineering Volume 1, Issue 9, May-2014, pp.1015-1017.

29. Naveen K B and M B Anandaraju, et.al, "Design and Synthesis of Bus Invert Encoding and Decoding Technique Using Reversible Logic" International Journal of Engineering Research and Applications (IJERA), Vol. 3, Issue 4, Jul-Aug 2013, pp.143-146.

30. Naveen KB and Prashantha NC, "Design and Synthesis of QPSK: A Subsystem Module of Digital Communication using Reversible logic" pp.53-57, IJCA (0975-8887), vol. 74, No. 10, July 2013.

31. Naveen K B and Prashanth N G, "Design of Efficient Reversible Fault tolerant Adder/Subtractor", International Journal of Computer Applications (0975 – 8887) Volume 74– No.9, July 2013, pp 23-28.

32. Naveen K B and Sushmitha B C, "Efficient Error Detection and Correction For Memory Applications Using Majority Logic Decoder and Detection", IJIRD, Vol. 2, Issue 5, 2013, pp 1157-1171.

INTERNATIONAL CONFERENCE:

1. Naveen K B et.al, " Internet of Things: A Survey on Devices, Ecosystem, Components and Communication Protocols" IEEE, 4th International Conference o Electronics, Communication and Aerospace Technology (ICECA-2020), organized by RVS Technical Campus, Coimbatore, India on 5-7, November 2020.

2. Naveen K B, Puneeth G S et.al, "Low Power Viterbi Decoder Design based on Reversible Logic Gates" 4th International Conference in Electronics and Communication Systems, IEEE, 2017, PP 201-205.

3. Naveen K B and Dr. M N Sree Rangaraju, "Low Power Architecture for 17X17 parallel multiplier using Reversible logic gate in 65nm Technology", IEEE International Conference on " Computing for sustainable Global development Indiacom-2017, organized by BVICAM, New Delhi on 01st to 03rd March, 2017.

4. Naveen K B et.al, " Low power high performance performance wallace tree multiplier", 3rd National Conference on emerging trends in electronics and communication (NCETEC 16) on 4th MAY 2016.

5. Naveen K B et.al, "Convolution encoder design using reversible logic", 3rd National

Conference on emerging trends in electronics and communication (NCETEC-16) on 4th MAY 2016.

6. Naveen K B and Yogananda C D, " Convolutional Encoder Design using 2-Bit Reversible Logic", National conference, BGSIT, ISBN: 978-93-84698-12-6, 2016.

7. Naveen K B and Dr. M. B Anandaraju, et.al, "Low Power High Performance Booth Encoded Wallace Tree Multipliers", National conference, BGSIT, ISBN: 978-93-84698-12-6, 2016.

8. Naveen K B and Divya K P, "Development of 8 bit sequence generator" ISRASE eXplore Digital Library, ISBN: 978-93-84698-12-6, 2015.

9. Naveen K B and Puneeth GS "Micro Hybrid Electric Vehicle for Real Time Applications" National Conference on Advanced Research in Electronics and Communication Engineering, April 2014, RLJIT Doddaballapur.

10. Naveen K B and Puneeth G S, "Automatic Fire Extinguisher in Train", ISRASE eXplore Digital Library, ISRASE 2015.

11. Naveen K B and Hamsa Lekha " GPS based Intelligent Vehicle tracking and monitoring system", 6th National Conference on "Recent Trends in Electronics and Communication Engineering, organized by department of electronics and communication engineering, on 10th may 2014 at SJB Institute of Technology, Bangalore.

12. Naveen K B and Hamsalekha N P et.al, "Improving Efficiency and Performance in a Public Cloud Partitioning Via Load Balancing" National conference, BGSIT, 2014, pp 401-405.

13. Naveen K B et.al., " Micro Hybrid Electric Vehicle for Real Time Application" Paper ID NCAREC-ES-024, Advanced Research in electronics and Communication engineering - 2014, RL Jalappa Institute of Technology, doddaballapur on 22nd & 23rd April 2014.

14. Naveen KB and Prashantha NC, et.al, "Digital System Design BPSK: A Subsystem Module of Digital Communication using Reversible logic" pp.367-369, National Conference on Recent Trends in Electronics and Communication Engineering, GMIT, June 2013.

15. Naveen KB, Subramanya BA, "Zigbee based Automation system for Smart home" at National conference on current trends in Electronics and Communication Engineering" SJBIT, Bangalore, May 2013.

WORKSHOPS/SEMINARS/FDPs/ PARTICIPATED:

1. Faculty Development Programme on "Recent Develop in Power Systems" Organized by department of EEE from 23-27 November 2020, Rajeev Institute o Technology, Hassan

2. Faculty Development Programme on "Advances in Signal Processing and Wireless Communication" Organized by department of ECE, Rajeev Institute of Tcehnology, Hassan from 24-29 August 2020.

3. One Week STTP on Advanced Research Methodology from 11th May 2020 to 15th May 2020, Organised by REST society for Research International (RSRI). Krishanagiri, Tamilnadu, India.

4. Skill Enhancement Programme on "Python for Data Science", conducted by Forsk coding school, held between 18-25 April, 2020.

5. Three days International Conference on "11th Bengaluru India Nano" from 2nd March, 2020 to 4th March 2020 at Lalit Ashok, Bengaluru.

6. IEEE Faculty Conclave, Organized by IEEE Bangalore Section at NIE Institute of Technology, Mysuru on 29th February 2020.

7. Training on "IPR & Technolgy Transfer" organized by AICTE/MHRD's Innovation Cell on 26-02-2020 & 27-02-2020 held at ACS College of Engineering, Bengaluru.

8. Jnana-Vijnana-Tantrajnana mela -2020 which was held on 19th & 20th February, 2020 at Sri kshetra Adichuchnagairi.

9. Faculty Development Program on "Machine Learning and Its Applications organized by Department of CSE, BGSIT from 22nd to 27th of January 2020.

10. Workshop on "Arduino and IOT" in association with E-PARK Research Center at Rajjev Institute of Technology, Hassan on 08-09-2019 to 10-09-2019.

11. Orientation Program on "Draft National Education Policy-2019" held on 06th September 2019 at Adichunchanagiri University. BG Nagara.

12. Training on Management of Intellectual property organized by Adichunchanagiri University in association with KSCST, Govt. of Karnataka on 21st June 2019.

13. Two week faculty development programme on Entrepreneurship held during 17th to 28th of January 2019, conducted by department of SJB Technology Business Incubation center.

14. International Symposium on Nanomaterials-2018 (NANOMAT-2018) held during 22nd September, 2018 at Department of Nanotechnology, Visvesvaraya Technological University, Center for Postgraduate studies, Bengaluru Region, Muddenahalli.

15. Six Days Faculty development Programme on "CCNA Switching and Routing" from 23rd July 2018 to 28th July 2018 at SJB Institute of Technology, Bangalore.

16. VGST Sponsored faculty development Programme on "Emerging Innovative approaches

in VLSI, Communication and Network Systems" organized by department of ECE, BGSIT from 10th April 2018 to 13th April 2018.

17. Workshop on "Image Processing and Its Applications in MATLAB" in association with Vidvath Technologies private limited, organized by Department of ECE, Rajeev Institute of Technology, Hassan from 21st to 23rd February 2018.

18. 6-Day Faculty development prgramme on "Embedded Controller Programming with Arm Cortex-M3, organized by department of ECE, BGSIT from 29th January 2018 to 03rd February 2018.

19. National seminar "Kaushala-2017", on skill module for engineers and managers, at Alva's institute of engineering & technology, Moodbidri on 21st & 22nd April 2017.

20. Participated in volunteer of technology experience, a one day volunteering event organized by loginware softtec pvt.Ltd on 4th June 2017.

21. Two Days workshop on use of E-resources for Teaching and Research , held on 23rd and 24th June 2017 at BGS Institue of Technology, BG Nagara in association with Visvesvaraya Technological University, Belagavi.

22. Jnana-Vijnana-Tantrajnana mela -2017 which was held on 20th &21st February, 2017 at Sri kshetra Adichuchnagairi.

23. 2 day workshop on "Internet of things" at BGS Institute of technology, conducted from 21st & 22nd march, 2017 organized by department of Electronics and communication engineering and Robokart in association with Innovation cell UNIC, IIT Bombay.

24. One week FDP on "Bridge the Gap-VTU CBCS Scheme", organized by the department of CSE/ISE/MCA, Rajarajeshwari college of Engineering, Bengaluru in association with computer society of india held during 19th to 25th July 2016.

25. Participated as a delegate in the event BengaluruITE.biz 2016 organized from Department of IT, BT & Science and Technology, Government of Karnataka and software technology parts of INDIA from 28th to 30th November 2016 at Bengaluru palace, Bangalore.

26. Participated as a delegate in the international conference at 8th Bangalore India Nano 2016 at The Lalit Ashok on 3rd & 4th March 2016.

27. TEQIP – II sponsored one week faculty development programme on " advances in vlsi design" organized by department of Electronics and Communication engineering, government SKSJT Institute , Bangalore from 5th October to 9th October 2015.

28. Participated in Jnana-Vijnana mela -2015 which was held on 19th & 20th February, 2015 at Sri kshetra Adichuchnagairi.

29. Participated in Jnana-Vijnana mela -2014 which was held on 19th & 20th February, 2014 at Sri kshetra Adichuchnagairi.

30. One day seminar on " Mysteries of our universe-A scientific Inquiry" organized at BGS Institute of Technolgy, BG Nagar on 31st October 2014.

31. Three day workshop on " Analog and digital VLSI design using cadence tools" at Rajeev Institute of Technology, Hassan from 28th April to 30th April 2014.

32. Three day workshop on " Research Methodologies and Latex", organized by VTU e learning centre, Mysore from 8th to 10th july, 2014 at VTU Hq, Belgaum.

33. 4-day VTU-VGST faculty development program on "VLSI System on Chip design and Validation", from 10th to 13th July 2013 organized by R & D Centre and ECE department, MS Engineering College, Bangalore.

34. SAP Techniversity India's largest Technology Event on 28th September 2013, SAP Labs India.

35. International conference on Optical Engineering (ICOE-2012) during 26th to 28th July 2012 at Visvesvaraya Technological University Campus, Belgaum, Karnataka, INDIA.

36. One Week VTU-VGST Facculty development program on "Advances in Communication and Network Systems" from 12th to 16th March 2012, organized by department of Electronics and Communication Engineering , BGS Institute of Technology, BG Nagara.

37. One day workshop on "MSP430 & Applications" conducted by the department of Electronics and communication engineering in association with EdGate Technologies, Bangalore on 31st January 2012 at Ghousia College of engineering, Ramanagaram.

38. National Conference on " VLSI and Communication Systems" held at Bangalore institute of technology, Bangalore on 30th & 31st October, 2012.

39. 3-Day National Level Workshop on " ARM9/11 Archtecture and its Applications" organized by E&C/TV departments in association with Sitech Electronics & IETE Bengaluru Center on 27th to 29th January, 2011, SJC Institute of Technology, Chickballapur.

40. Participated as a delegate in the international conference at 5th Bangalore India Nano 2012 at The Lalit Ashok on 5th to 7th December, 2012.

41. Two-day Faculty development programme on " recent Trends in Front End VLSI system design" conducted by the department of Electronics and communication engineering on 30th & 31st March 2011 at BGS Institute of Technology, BG Nagara.

OTHER RESPONSIBILITIES

1. Working as centre manager in Skill development programmes funded by Government of Karnataka.

2. Working as a Department In-charge HOD from January 2017.

3. Working as a Co-coordinator in several VGST - BGSIT programs.

4. Worked as Seminar and Project Co-ordinator in both UG and PG

programs. 5. Worked as IA Co-ordinator in the department of ECE.

6. Working as NBA Co-ordinator in the department of ECE.

7. Act as Chairperson in "Technisium -2017" held on 18-03-2017 at Siddaganga Institute of Technology, Tumkuru.

8. Act as Chairperson in "National Technical Symposium, Technisium-2018 held on 03-03- 2018 at Siddanganga Institue of Technology, Tumkuru.

9. Worked as a project Co-ordinator for both B.E and M.Tech programs in department of ECE.

10. Organized faculty development programmes and workshops in department of ECE.

RESEARCH SUPERVISOR

1. Visveswaraya Technological University, Belagavi
2. Adichunchanagiri University, BG Nagara

AWARDS, COMMENDATIONS RECEIVED

1. Received 1st Prize in project exhibition "Jnana Vijnana Tantrajnana Mela" at Adichunchanagiri Mutt in the year 2020.

2. Received 1st Prize in project exhibition "Jnana Vijnana Tantrajnana Mela" at Adichunchanagiri Mutt in the year 2019.

3. Certificate of Appreciation for outstanding honorable effort for the project "Monitoring of Ozone in ambient air and controlling of oxygen generator with RTC" at Loginware Softtec pvt. Ltd during 2017-18.

4. Received best teacher Award Received Best Teacher Award from BGS Institute of Technology in recognition of valuable contributions in academics & co-curricular activities of the institution for the year 2016-2017.

PATENTS

1. Smart Kitchen Using Altering System and IOT - National
2. Method of Auto Tuning PID Controller using Evolutionary Computational Technique (International –USA) – In Progress
3. Method of Lung Cancer Detection by using Deep Learning Technique- (International – USA) – In Progress
4. Content Based Image Retrieval for ECG Reports using Data Analytics- (National) – In Progress

5. Method for Detection and Localization of Dental Caries using Deep Learning- (National) – In Progress

GRANTS RECEIVED FOR MAJOR AND MINOR PROJECTS

Title of the project	Sanctioned Year	Agency	Amount
New Age Incubation Network (NAIN) –College Co ordinator	2020-21	Karnataka Innovation and Technology Society	--
Electronic Based Water Management Center for Public Sector and Rural Areas	2020-21	Adichunchanagiri University	Rs. 2,40,000/-
Design and Development An Algorithm for Canny Edge Detector for Lung Cancer Detection	2020-21	Adichunchanagiri University	Rs. 1,10,000/-
An Early Detection of Exudates In Retinal Fundus Images Using SVM Algorithm	2019-20	Karnataka State Council for Science and Technology (KSCST)	Rs. 3,500/-
Farmer Friend: IoT Based Smart Polyhouse	2019-20	Karnataka State Council for Science and Technology (KSCST)	Rs. 4,000/-

Entrepreneurship Awareness Camp	2019-20	National Science & Technology Entrepreneurship Development Board (NSTEDB), Department of Science and Technology, Government of India, New Delhi	Rs. 1,40,000/-