



|| Jai Sri Gurudev ||

BGS Institute of Technology

BG Nagara - 571448

Department of Electronics and Communication Engineering

Academic Year 2016-17 (Odd Semester) Master Time Table

3rd A						15MAT31	Engg. Mathematics-III	Rashmi B T				
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC32	Analog Electronics	Ramya K	
M	32	34	33	35		AEC/DE lab				15EC33	Digital Electronics	Kokila K S
T	35	31	34	36		HR Training				15EC34	Network Analysis	Puneeth Kumar G B
W	33	31	32	35		AEC/DE lab				15EC35	Electronic Instrumentation	Kavitha G K
T	31	36	32	33		AEC/DE lab				15EC36	Engineering Electromagnetics	Prabhavathi K
F	34	31	35	36			32	36	35	15ECL37	Analog Electronics Lab	Ramya K/ Puneeth Kumar G B / Raghavendra L R
S	36	32	34	33			34	31	33	15ECL38	Digital Electronics Lab	Kavitha G K / Kokila K S
3rd B							15MAT31	Engg. Mathematics-III	Parashiva Murthy H L			
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC32	Analog Electronics	Ramya K	
M	35	31	36	34		HR Training				15EC33	Digital Electronics	Kokila K S
T	33	36	32	35		AEC/DE lab				15EC34	Network Analysis	Puneeth Kumar G B
W	34	AEC/DE lab				33	31	32	15EC35	Electronic Instrumentation	Kavitha G K	
T	34	35	31	36		33	31	34	15EC36	Engineering Electromagnetics	Prabhavathi K	
F	36	33	34	32		AEC/DE lab				15ECL37	Analog Electronics Lab	Puneeth Kumar G B/Ramya K/ Raghavendra
S	31	33	32	35			32	36	35	15ECL38	Digital Electronics Lab	Kokila K S / Kavitha G K
5th A							10AL51	Management and Entrepreneurship	Prashantha N C			
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	10EC52	Digital Signal Processing	Anusha M N	
M	53	54	52	51		DSP/AC lab				10EC53	Analog Communication	Mohan Kumar K S
T	HR Training		51	54		DSP/AC lab				10EC55	Microwaves and Radar	Balaji B S
W	56	53	55	52		DSP/AC lab				10EC54	Information Theory& Coding	Nandini S
T	52	56	51	55			53	52	56	10EC56	Fundamentals of CMOS VLSI	Nandeesh M
F	56	54	55	53			52	51		10ECL57	DSP Lab	Anusha M N /Nandini S /Manoj Kumar S B
S	56	53	54	55						10ECL58	Analog Communication+ Lic Lab	Prabhavathi K / Mohan Kumar K S
5th B							10AL51	Management and Entrepreneurship	Prashantha N C			
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	10EC52	Digital Signal Processing	Anusha M N	
M	HR Training		56	54		53	55	51	10EC53	Analog Communication	Mohan Kumar K S	
T	52	DSP/AC lab				53	56	55	10EC55	Microwaves and Radar	Balaji B S	
W	55	52	51	53		53	56		10EC54	Information Theory& Coding	Nandini S	
T	54	55	52	53		DSP/AC lab				10EC56	Fundamentals of CMOS VLSI	Nandeesh M
F	52	51	56	54		DSP/AC lab				10ECL57	DSP Lab	Nandini S/Anusha M N/ Manoj Kumar S B
S	54	51	56	52						10ECL58	Analog Communication+ Lic Lab	Mohan Kumar K S / Prabhavathi K
7th A							10EC71	Computer Communication Network	Amar Narayana			
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	10EC72	Optical Fiber Communications	Madhurya B Eshwar	
M	71	751	762	73		PE/VLSI LAB				10EC73	Power Electronics	Punith Kumar M B
T	72	74	73	751			71	762		10EC74	Embedded System Design	Ashwini S R
W	73	PE/VLSI LAB					74	751	72	10EC751	DSP Algorithms and Architecture	Naveen K B

T	762	74	73	751		72	71		10EC762	Real Time Systems (Elective C)	Manoj Kumar S B	
F	74	72	762	71		PE/VLSI LAB			10ECL77	VLSI Lab	Shalini K J /Balaji B S / Naveen K B	
S	Project Work								10ECL78	Power Electronics Lab	Nandeesh M/ Prashantha N C/ Punith Kumar M B	
7th B												
									10EC71	Computer Communication Network	Amar Narayana	
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	10EC72	Optical Fiber Communications	Madhurya B Eshwar	
M	74	73	72	751		71	762	72	10EC73	Power Electronics	Punith Kumar M B	
T	73	751	71	74		PE/VLSI LAB			10EC74	Embedded System Design	Ashwini S R	
W	74	72	71	762		PE/VLSI LAB			10EC751	DSP Algorithms and Architecture	Naveen K B	
T	73	751	72	762		PE/VLSI LAB			10EC762	Real Time Systems (Elective C)	Manoj Kumar S B	
F	762	71	751	73		74	72		10ECL77	VLSI Lab	Shalini K J /Balaji B S / Naveen K B	
S	Project Work									10ECL78	Power Electronics Lab	Nandeesh M/ Prashantha N C/ Punith Kumar M B

Signature of HOD