



**|| Jai Sri Gurudev ||**  
**BGS Institute of Technology**  
**BG Nagara - 571448**

**Department of Electronics and Communication Engineering**  
**Academic Year 2017-18 ( Even Semester) Master Time Table**

| 4th A |    |    |     |    |             |              |    | 15MAT41 | Engg. Mathematics-IV | Parashivamurthy H L |                                    |   |
|-------|----|----|-----|----|-------------|--------------|----|---------|----------------------|---------------------|------------------------------------|---|
| Days  | I  | II | III | IV | LUNCH BREAK | V            | VI | VII     | 15EC42               | Microprocessor      | Dr. Naveen B                       |   |
| M     | 45 | 46 | 41  | 42 |             | HR           |    |         |                      | 15EC43              | Control System                     | Puneeth Kumar G B                             |
| T     | 44 | 45 | 41  | 43 |             | MP / LIC LAB |    |         |                      | 15EC44              | Signals and System                 | Anusha M N                                    |
| W     | 44 | 42 | 45  | 46 |             | MP / LIC LAB |    |         |                      | 15EC45              | Principles of Communication system | Priyanka R                                    |
| T     | 43 | 45 | 44  | 46 |             | 41           | 42 | 43      |                      | 15EC46              | Linear Integrated Circuits         | Kavitha B C                                   |
| F     | 42 | 43 | 44  | 46 |             | 46           | 45 | 41      |                      | 15ECL47             | Microprocessor Lab                 | Prashantha N C                                |
| S     | 43 | 42 | 41  | 44 |             |              |    |         |                      | 15ECL48             | LIC Lab                            | Dr. M B Anandaraju/<br>Kavitha B C/Anusha M N |

| 4th B |    |              |     |    |             |              |    | 15MAT41 | Engg. Mathematics-IV | Chaithra       |                                    |                             |
|-------|----|--------------|-----|----|-------------|--------------|----|---------|----------------------|----------------|------------------------------------|-----------------------------|
| Days  | I  | II           | III | IV | LUNCH BREAK | V            | VI | VII     | 15EC42               | Microprocessor | Shalini K J                        |                             |
| M     | 41 | 42           | 43  | 45 |             | 46           | 44 | 41      |                      | 15EC43         | Control System                     | Puneeth Kumar G B           |
| T     | 43 | 45           | 41  | 46 |             | HR           |    |         |                      | 15EC44         | Signals and System                 | Kokila K S                  |
| W     | 45 | 42           | 44  | 43 |             | 44           | 46 | 43      |                      | 15EC45         | Principles of Communication system | Prafulla P S                |
| T     | 42 | MP / LIC LAB |     |    |             | 41           | 42 | 45      |                      | 15EC46         | Linear Integrated Circuits         | Nandini S                   |
| F     | 44 | 46           | 42  | 43 |             | MP / LIC LAB |    |         |                      | 15ECL47        | Microprocessor Lab                 | Dr. Naveen B/Kokila K S     |
| S     | 41 | 46           | 45  | 44 |             |              |    |         |                      | 15ECL48        | LIC Lab                            | Puneeth Kumar G B/Nandini S |

| 4th C |    |    |     |    |             |              |    | 15MAT41 | Engg. Mathematics-IV | Yuvaraju B K   |                                    |                                 |
|-------|----|----|-----|----|-------------|--------------|----|---------|----------------------|----------------|------------------------------------|---------------------------------|
| Days  | I  | II | III | IV | LUNCH BREAK | V            | VI | VII     | 15EC42               | Microprocessor | Shalini K J                        |                                 |
| M     | 43 | 44 | 45  | 46 |             | MP / LIC LAB |    |         |                      | 15EC43         | Control System                     | Mohan Kumar k S                 |
| T     | 42 | 41 | HR  |    |             | 46           | 43 | 45      |                      | 15EC44         | Signals and System                 | Anusha M N                      |
| W     | 45 | 46 | 41  | 42 |             | 41           | 42 | 44      |                      | 15EC45         | Principles of Communication system | Priyanka R                      |
| T     | 44 | 46 | 43  | 42 |             | MP / LIC LAB |    |         |                      | 15EC46         | Linear Integrated Circuits         | Kavitha B C                     |
| F     | 42 | 46 | 45  | 43 |             | 41           | 44 | 43      |                      | 15ECL47        | Microprocessor Lab                 | Jayanth Dwijesh H P/Shalini K J |
| S     | 43 | 44 | 45  | 41 |             |              |    |         |                      | 15ECL48        | LIC Lab                            | Mohan Kumar k S/Sandeep S V     |

| 6th A |     |     |     |     |             |              |     | 15EC61 | Digital Communication | Prabhavathi K |                           |                              |
|-------|-----|-----|-----|-----|-------------|--------------|-----|--------|-----------------------|---------------|---------------------------|------------------------------|
| Days  | I   | II  | III | IV  | LUNCH BREAK | V            | VI  | VII    | 15EC62                | ARM           | Balaji B S                |                              |
| M     | 64  | 61  | 655 | 62  |             | Em / CCN Lab |     |        |                       | 15EC63        | VLSI Design               | Srividya C N                 |
| T     | HR  |     | 63  | 64  |             | 62           | 661 | 65     |                       | 15EC64        | CCN                       | Nischitha S                  |
| W     | 661 | 61  | 63  | 655 |             | Em / CCN Lab |     |        |                       | 15EC655       | Microelectronics          | Nandeesh M                   |
| T     | 63  | 661 | 61  | 62  |             | Em / CCN Lab |     |        |                       | 15EC661       | Data Structures Using C++ | Manoj Kumar S B              |
| F     | 62  | 655 | 661 | 655 |             | 61           | 64  | 63     |                       | 15ECL67       | Embedded Controller Lab   | Manoj Kumar S B/Prafulla P S |
| S     | 61  | 64  | 62  | 63  |             |              |     |        |                       | 15ECL68       | Computer Networks Lab     | Prabhavathi K/Nischitha S    |

| 6th B |     |              |     |     |             |              |    | 15EC61 | Digital Communication | Prabhavathi K |                           |                            |
|-------|-----|--------------|-----|-----|-------------|--------------|----|--------|-----------------------|---------------|---------------------------|----------------------------|
| Days  | I   | II           | III | IV  | LUNCH BREAK | V            | VI | VII    | 15EC62                | ARM           | Balaji B S                |                            |
| M     | HR  |              | 661 | 61  |             | 64           | 63 | 655    |                       | 15EC63        | VLSI Design               | Srividya C N               |
| T     | 63  | 655          | 62  | 661 |             | Em / CCN Lab |    |        |                       | 15EC64        | CCN                       | Nischitha S                |
| W     | 655 | 64           | 661 | 61  |             | 655          | 62 | 63     |                       | 15EC655       | Microelectronics          | Nandeesh M                 |
| T     | 655 | Em / CCN Lab |     |     |             | 661          | 61 | 62     |                       | 15EC661       | Data Structures Using C++ | Manoj Kumar S B            |
| F     | 64  | 61           | 62  | 63  |             | Em / CCN Lab |    |        |                       | 15ECL67       | Embedded Controller Lab   | Nandeesh M/Srividya C N    |
| S     | 62  | 63           | 61  | 64  |             |              |    |        |                       | 15ECL68       | Computer Networks Lab     | Raghavendra L R/Priyanka R |

| 8th A |     |     |     |     |             |                   |    |     |        |                        |                           |                                  |
|-------|-----|-----|-----|-----|-------------|-------------------|----|-----|--------|------------------------|---------------------------|----------------------------------|
| Days  | I   | II  | III | IV  | LUNCH BREAK | V                 | VI | VII | 10EC81 | Wireless Communication | Naveen K B                |                                  |
| M     | 835 | 81  | 843 | 82  |             | Project / Seminar |    |     |        | 10EC82                 | Digital Switching Systems | Nandini S                        |
| T     | 82  | 835 | 843 | 81  |             | Project / Seminar |    |     |        | 10EC835                | Network Security          | Jayanth Dwijesh H P              |
| W     | 843 | 82  | 835 | 81  |             | Project / Seminar |    |     |        | 10EC843                | GSM                       | Sandeep S V                      |
| T     | 82  | 81  | 835 | 843 |             | Project / Seminar |    |     |        | 10ECP85                | Project Co-ordinator      | Dr. MB Anandaraju/<br>Nandeesh M |

|          |                   |  |  |  |                   |  |  |         |                      |                                |
|----------|-------------------|--|--|--|-------------------|--|--|---------|----------------------|--------------------------------|
| <b>F</b> | Project / Seminar |  |  |  | Project / Seminar |  |  | 10ECS86 | Seminar Co-ordinator | Prashanth N C/ Raghavendra L R |
| <b>S</b> | Project / Seminar |  |  |  |                   |  |  |         |                      |                                |

| 8th B    |                   |     |     |     |             |                   |    |     |  |         |                           |                                     |
|----------|-------------------|-----|-----|-----|-------------|-------------------|----|-----|--|---------|---------------------------|-------------------------------------|
| Days     | I                 | II  | III | IV  | LUNCH BREAK | V                 | VI | VII |  |         |                           |                                     |
| <b>M</b> | 843               | 82  | 835 | 81  |             | Project / Seminar |    |     |  | 10EC81  | Wireless Communication    | Naveen K B                          |
| <b>T</b> | 843               | 81  | 82  | 835 |             | Project / Seminar |    |     |  | 10EC82  | Digital Switching Systems | Prashantha N C                      |
| <b>W</b> | 835               | 81  | 843 | 82  |             | Project / Seminar |    |     |  | 10EC835 | Network Security          | Jayanth Dwijesh H P                 |
| <b>T</b> | 835               | 843 | 82  | 81  |             | Project / Seminar |    |     |  | 10EC843 | GSM                       | Sandeep S V                         |
| <b>F</b> | Project / Seminar |     |     |     |             | Project / Seminar |    |     |  | 10ECP85 | Project Co-ordinator      | Dr. MB Anandaraju/<br>Nandeesh M    |
| <b>S</b> | Project / Seminar |     |     |     |             | Project / Seminar |    |     |  | 10ECS86 | Seminar Co-ordinator      | Prashanth NC/Mr.<br>Raghavendra L R |
|          |                   |     |     |     |             |                   |    |     |  |         |                           |                                     |

**Signature of HOD**