



|| Jai Sri Gurudev ||
BGS Institute of Technology
BG Nagara - 571448

**Department of Electronics and Communication Engineering
Academic Year 2017-18 (Odd Semester) Master Time Table**

3rd A						15MAT31	Engg. Mathematics-III	Shwetha H N			
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC32	Analog Electronics	Kokila K S
M	35	36	34	33		HR			15EC33	Digital Electronics	Kavitha B C
T	31	34	32	33		35	34	31	15EC34	Network Analysis	Raghavendra L R
W	35	AEC(A1)/DE (A2)				31	32	36	15EC35	Electronic Instrumentation	Sri Vidya C N
T	36	33	32	31		35	33	34	15EC36	Engineering Electromagnetics	Dr. Manjunatha R C
F	36	35	31	32		AEC(A2) / DE(A1)			15ECL37	Analog Electronics Lab	Raghavendra L R
S	32	36	33	34					15ECL38	Digital Electronics Lab	Nandini S
3rd B							15MAT31	Engg. Mathematics-III	Shwetha H N / Rashmi B T		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC32	Analog Electronics	Kokila K S
M	31	36	33	32		AEC(B1) / DE (B2)			15EC33	Digital Electronics	Nandini S
T	35	33	34	36		HR			15EC34	Network Analysis	Puneeth Kumar G B
W	32	36	31	35		AEC(B2) / DE(B1)			15EC35	Electronic Instrumentation	Sri Vidya C N
T	32	31	34	36		34	33	35	15EC36	Engineering Electromagnetics	Prabhavathi K
F	34	32	33	35		31	36	31	15ECL37	Analog Electronics Lab	Dr. M B Anandaraju /Kavitha B C
S	33	34	32	35					15ECL38	Digital Electronics Lab	Puneeth Kumar G B
3rd C							15MAT31	Engg. Mathematics-III	Yuvaraja B K		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC32	Analog Electronics	Kavitha B C
M	33	32	34	36		33	31	35	15EC33	Digital Electronics	Nandini S
T	34	36	HR			AEC(C1) / DE(C2)			15EC34	Network Analysis	Puneeth Kumar G B
W	34	32	31	35		36	33		15EC35	Electronic Instrumentation	Prafulla P S
T	31	36	33	32		AEC(C2) / DE(C1)			15EC36	Engineering Electromagnetics	Prabhavathi K
F	35	32	36	34		35	32	31	15ECL37	Analog Electronics Lab	Kokila K S
S	35	31	33	34					15ECL38	Digital Electronics Lab	Prabhavathi K
5th A							15ES51	Management and Entrepreneurship	Balaji B S		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC52	Digital Signal Processing	Anusha M N
M	54	562	51	53		DSP(A1)/HDL(A2)			15EC53	Verilog HDL	Shalini K J
T	HR		54	562		53	52	553	15EC54	Information Theory & Coding	Jayanth Dwijesh H P
W	51	562	553	52		DSP(A3)/HDL(A1)			15EC553	Operating System	Prashanth N C
T	53	DSP(A2)/HDL(A3)				553	54	51	15EC562	Oops Using C++	Manoj Kumar S B
F	52	562	553	53		51	54	52	15ECL57	DSP Lab	Anusha M N
S	562	53	52	54					15ECL58	HDL Lab	Sri Vidya C N
5th B							15ES51	Management and Entrepreneurship	Balaji B S		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC52	Digital Signal Processing	Anusha M N
M	HR		52	54		53	562	553	15EC53	Verilog HDL	Shalini K J
T	51	562	53	52		DSP(B1)/HDL(B2)			15EC54	Information Theory & Coding	Jayanth Dwijesh H P
W	53	52	51	54		553	562	53	15EC553	Operating System	Prashanth N C
T	562	51	553	54		DSP(B2)/HDL(B3)			15EC562	Oops Using C++	Manoj Kumar S B
F	53	54	52	562		DSP(B3)/HDL(B1)			15ECL57	DSP Lab	Mohan Kumar K S
S	52	54	553	51					15ECL58	HDL Lab	Manoj Kumar S B
7th A							10EC71	Computer Communication Network	Amar Narayan		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	10EC72	Optical Fiber Communications	Dr. Manjunatha R C
M	74	73	762	72		VLSI(A1) / PE(A3)			10EC73	Power Electronics	Nandeesh M
T	73	72	762	751		VLSI(A2) / PE(A1)			10EC74	Embedded System Design	Priyanka R
W	73	74	751	71		VLSI(A3) / PE(A2)			10EC751	DSP Algorithms and Architecture	Naveen K B
T	762	751	73	71		74	72	751	10EC762	Real Time Systems (Elective C)	Sandeep S V
F	74	71	73	751		762	72	71	10ECL77	VLSI Lab	Balaji B S

S	762	71	74	72				10ECL78	Power Electronics Lab	Jayanth Dwijesh H P	
7th B											
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	10EC71	Computer Communication Network	Nischitha S
M	71	751	74	73		762	72	74	10EC72	Optical Fiber Communications	Dr. Manjunatha R C
T	762	751	74	72		73	751	71	10EC73	Power Electronics	Nandeesh M
W	72	VLSI(B1)/PE(B3)				751	762	73	10EC74	Embedded System Design	Priyanka R
T	71	74	72	751		VLSI(B2) / PE(B1)			10EC751	DSP Algorithms and Architecture	Naveen K B
F	73	762	72	71		VLSI(B3) / PE(B2)			10EC762	Real Time Systems (Elective C)	Sandeep S V
S	74	73	762	71					10ECL77	VLSI Lab	Shalini K J
									10ECL78	Power Electronics Lab	Nandeesh M

Signature of HOD