



|| Jai Sri Gurudev ||
BGS Institute of Technology
BG Nagara - 571448

Department of Electronics and Communication Engineering
Academic Year 2018-19 (Even Semester) Master Time Table

4th A											
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII			
M	41	42	45	43		46	HR		17EC42	Engg. Mathematics-IV	Mr. Parashivamurthy H L
T	43	Lab				44	41	42	17EC43	Signals and System	Mrs. Kokila K S
W	44	43	46	45		Lab			17EC44	Control System	Mrs. Prabhavathi K
T	46	44	42	41		Lab			17EC45	Principles of Communication system	Mrs. Priyanka R
F	45	42	44	43		46	41	45	17EC46	Linear Integrated Circuits	Mrs. Kavitha B C
S	42	45	44	43					17ECL47	Microprocessor	Dr. Naveen B
									17ECL48	Microprocessor Lab	Dr. Naveen B/Mr. Jayanth Dwijesh HP
									LIC Lab	Dr. MB Anandaraju/Mr. Mohan Kumar KS	
4th B											
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII			
M	44	43	41	42		Lab			17MAT41	Engg. Mathematics-IV	Dr. Yuvaraja B K
T	46	44	41	42		45	HR		17EC42	Signals and System	Mrs. Kokila K S
W	42	Lab				43	45	44	17EC43	Control System	Mrs. Prabhavathi K
T	45	43	46	44		43	42	41	17EC44	Principles of Communication system	Mrs. Priyanka R
F	44	43	46	45		Lab			17EC45	Linear Integrated Circuits	Mrs. Kavitha B C
S	41	46	42	45					17EC46	Microprocessor	Dr. Naveen B
									17ECL47	Microprocessor Lab	Mrs. Kokila KS/Mr. Jayanth Dwijesh HP
								17ECL48	LIC Lab	Mrs. Priyanka R/ Mr. Simha DKL N	
6th A											
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII			
M	61	64	661	63		Lab			15EC61	Digital Communication	Ms. Prafulla P S
T	HR		62	61		63	654	64	15EC62	ARM	Mr. Balaji B S
W	654	61	64	661		Lab			15EC63	VLSI Design	Dr. Naveen K B
T	62	63	661	61		Lab			15EC64	CCN	Ms. Nischitha S
F	661	63	654	62					15EC654	Digital Switching System	Mrs. S Nandini
S	64	62	654	63		64	661	61	15EC661	Data Structures Using C++	Mr. Manoj Kumar S B
									15ECL67	Embedded Controller Lab	Ms. Srividya C N / Mr. Manoj Kumar S B
								15ECL68	Computer Networks Lab	Ms. Nischitha S	
6th B											
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII			
M	HR		62	64		654	61	661	15EC61	Digital Communication	Ms. Prafulla P S
T	63	661	64	654		Lab			15EC62	ARM	Mr. Balaji B S
W	63	Lab				661	62	61	15EC63	VLSI Design	Ms. Srividya C N
T	64	61	62	63		61	654	62	15EC64	CCN	Ms. Nischitha S
F	61	64	661	63		Lab			15EC654	Digital Switching System	Mrs. S Nandini
S	654	63	64	62					15EC661	Data Structures Using C++	Mr. Manoj Kumar S B
									15ECL67	Embedded Controller Lab	Mr. Balaji B S
								15ECL68	Computer Networks Lab	Mr. Raghavendra L R	
8th A											
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII			
M	81	835	82	85		85	85		15EC81	Wireless Cellular and LTE 4G Broadband	Mrs. Nandini S
T	835	81	82	85		85	85		15EC82	Fiber Optics & Networks	Ms. Bharathi R
W	82	835	81	85		85	85		15EC835	Network and Cyber Security	Mr. Jayanth Dwijesh H P
								15EC84	Internship/Professional Practice	Dr. Naveen K B/Mr. Simha D K L N	

T	82	81	835	85		85	85		15ECP85	Project Work	Mr. Manoj Kumar S B/Dr. M B Anandaraju/ Mr. Adarsh B G
F	85	85	85	85		85	85		15ECS86	Seminar	Dr. Naveen B / Mr. Raghavendra L R
S	86	86	86	86							
8th B											
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC81	Wireless Cellular and LTE 4G Broadband	Mrs. Kavitha B C
M	82	81	835	85		85	85		15EC82	Fiber Optics & Networks	Mr. Raghavendra L R
T	81	835	82	85		85	85		15EC835	Network and Cyber Security	Mr. Jayanth Dwijesh H P
W	82	81	835	85		85	85		15EC84	Internship/Professional Practice	Dr. Naveen K B/Mr. Simha D K LN
T	835	82	81	85		85	85		15ECP85	Project Work	Mr. Manoj Kumar S B/Dr. M B Anandaraju/ Mr. Adarsh B G
F	85	85	85	85		85	85		15ECS86	Seminar	Dr. Naveen B / Mr. Raghavendra L R
S	86	86	86	86							

Signature of HOD