



|| Jai Sri Gurudev ||
BGS Institute of Technology
BG Nagara - 571448

Department of Electronics and Communication Engineering
Academic Year 2018-19 (Odd Semester) Master Time Table

3rd A								17MAT31	Engg. Mathematics-III	Rashmi H	
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	17EC32	Electronic Instrumentation	Prafulla P S
M	34	31	35	36		32	HR		17EC33	Analog Electronics	Kokila K S
T	32	36	31	33		LAB			17EC34	Digital Electronics	Srividya C N
W	35	LAB				34	32	33	17EC35	Network Analysis	Raghavendra L R
T	31	36	33	35		LAB			17EC36	Engineering Electromagnetics	Prabhavathi K
F	32	34	33	36		31	35	34	17ECL37	Analog Electronics Lab	Dr. M B Anandaraju/ Kavitha B C / Kokila K S
S	33	34	35	36		33	36	31	17ECL38	Digital Electronics Lab	Prabhavathi K / Srividya C N
3rd B									17MAT31	Engg. Mathematics-III	Shwetha H N
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	17EC32	Electronic Instrumentation	Prafulla P S
M	32	36	33	31		LAB			17EC33	Analog Electronics	Kokila K S
T	35	33	34	32		36	HR		17EC34	Digital Electronics	Srividya C N
W	34	33	35	31		LAB			17EC35	Network Analysis	Raghavendra L R
T	33	35	34	36		32	31	35	17EC36	Engineering Electromagnetics	Prabhavathi K
F	31	36	32	34		LAB			17ECL37	Analog Electronics Lab	Kokila K S/Raghavendra L R
S	35	36	33	34		35	34	31	17ECL38	Digital Electronics Lab	Srividya C N / Priyanka R
5th A									15ES51	Management and Entrepreneurship	Balaji B S/Mr. Adarsh B G
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC52	Digital Signal Processing	Anusha M N
M	54	553	52	562		LAB			15EC53	Verilog HDL	Shalini K J
T	HR		54	51		553	52	53	15EC54	Information Theory & Coding	Jayanth Dwijesh H P
W	562	53	52	553		LAB			15EC553	Operating System	Priyanka R
T	51	53	562	54		LAB			15EC562	Oops Using C++	Manoj Kumar S B
F	553	54	51	562		52	53	553	15ECL57	DSP Lab	Anusha M N
S	53	52	54	51		562	51	52	15ECL58	HDL Lab	Shalini K J
5th B									15ES51	Management and Entrepreneurship	Balaji B S/Mr. Adarsh B G
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC52	Digital Signal Processing	Anusha M N
M	HR		53	553		51	562	54	15EC53	Verilog HDL	Shalini K J
T	553	562	53	52		LAB			15EC54	Information Theory & Coding	Jayanth Dwijesh H P
W	52	LAB				54	51	562	15EC553	Operating System	Priyanka R
T	562	54	52	53		54	553	51	15EC562	Oops Using C++	Manoj Kumar S B
F	51	53	553	52		LAB			15ECL57	DSP Lab	Jayanth Dwijesh H P
S	54	51	562	553		52	53	54	15ECL58	HDL Lab	Balaji B S
7th A									15EC71	Microwave and Antennas	Nandini S
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	15EC72	Digital Image Processing	Dr. Naveen B
M	752	72	71	743		LAB			15EC73	Power Electronics	Kavitha B C
T	72	73	752	71		72	71		15EC743	Real Time Systems	Bharathi R
W	743	743	72	71		LAB			15EC752	IoT & Wireless Sensor Networks	Nischitha S
T	71	743	71	72		LAB			15ECL76	Advanced Communication Lab	Nandini S/Mr. Simha DKLN
F	743	72	71	73		73	72		15ECL77	VLSI Lab	Dr. Naveen B
S	Project Work					Project Work			15ECL78	Project Phase-I + Project Seminar	Manoj Kumar S B/Raghavendra L R
7th B									15EC71	Microwave and Antennas	Nandini S
Days	I	II	III	IV	H BREAK	V	VI	VII	15EC72	Digital Image Processing	Dr. Naveen B
M	71	73	752	72		73	752		15EC73	Power Electronics	Kavitha B C

T	752	71	743	73	LAB	15EC743	Real Time Systems	Dr. Naveen K B		
W	752	LAB			73	752	743	15EC752	IoT & Wireless Sensor Networks	Nischitha S
T	72	752	743	73	752	73		15ECL76	Advanced Communication Lab	Nischitha S/Mr. Simha DKLN
F	71	73	752	743	LAB		15ECL77	VLSI Lab	Dr. Naveen K B/Manoj Kumar S B	
S	Project Work				Project Work		15ECL78	Project Phase-I + Project Seminar	Manoj Kumar S B/Raghavendra L R	

**Signature of
HOD**