

**|| Jai Sri Gurudev ||**  
**BGS Institute of Technology**  
**BG Nagara - 571448**  
**Dept. of Electronics and Communication Engg.,**  
**Dept. of ECE, Academic Year 2020-21 ( Odd Semester) Master Time Table**

3rd A								18MAT31	Engineering Mathematics-III	Dr. H L Parashivamurthy		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	18 EC32	Analog Electronics	Mrs. Ramya K	
M	33	31	34	36		Lab				18 EC33	Computer Organization & Architecture	Mrs. Hamsashree M K
T	32	34	36	35		Lab				18 EC34	Digital Electronics	Mrs. Kokila K S
W	36	31	33	35		Lab				18 EC35	Network Analysis	Dr. M B Anandaraju/ Mrs. Prabhavathi K
T	34	35	32	33						18 EC36	Engineering Electromagnetics	Mr. Mohan Kumar K S
F	31	32	35	36						18 ECL37	Analog Electronics Lab	Mrs. Kavitha B C / Mr. Goutham V
S	35	33	32	31						18 ECL38	Digital Electronics Lab	Mrs.Prabhavathi K/Mrs.Hamsashree M K

3rd B								18MAT31	Engineering Mathematics-III	Mrs. Chaithra S M		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	18 EC32	Analog Electronics	Mrs. Ramya K	
M	31	36	33	32						18 EC33	Computer Organization & Architecture	Mrs. Hamsashree M K
T	36	35	32	34						18 EC34	Digital Electronics	Mrs. Kokila K S
W	33	Lab					32	34	31	18 EC35	Network Analysis	Mrs. Prabhavathi K
T	36	33	34	35		Lab				18 EC36	Engineering Electromagnetics	Mr. Mohankumar K S
F	35	34	31	32		Lab				18 ECL37	Analog Electronics Lab	Mrs. Ramya K / Mr. Goutham V
S	36	31	35	33						18 ECL38	Digital Electronics Lab	Kokila K S/ Mrs.Hamsashree M K

5th A								18 EC 51	Management and Entrepreneurship	Mrs. Rashmi S		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	18 EC 52	Digital Communication	Mrs. Nandini S	
M	51	52	54	53		Lab				18 EC 53	Digital Signal Processing	Mrs. Anusha M N
T	54	52	55	561		Lab				18 EC 54	Information Theory and Coding	Mr. Goutham V/Mr. Jayanth Dwijesh H P
W	51	55	54	52		Lab				18 EC 55	Verilog HDL	Dr. Naveen K B/ Mr. Balaji B S
T	53	561	55	51						18 EC 562	Data Structure using C++	Mr. Manoj Kumar S B
F	52	53	561	55						18 ECL 57	DSP Lab	Mr. Jayanth Dwijesh H P/Ms. Lakshmi D L
S	561	51	53	54						18 ECL 58	HDL Lab	Dr. Naveen B/Mrs. Rashmi S/Divyashree R H

5th B								18 EC 51	Management and Entrepreneurship	Mrs. Rashmi S		
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	18 EC 52	Digital Communication	Mrs. Nandini S	
M	54	53	51	52						18 EC 53	Digital Signal Processing	Mrs. Anusha M N
T	55	51	53	52						18 EC 54	Information Theory and Coding	Mr. Goutham V/Mr. Jayanth Dwijesh H P
W	54	Lab					561	53	55	18 EC 55	Verilog HDL	Dr. Naveen K B/ Mr. Balaji B S
T	55	51	52	561		Lab				18 EC 562	Data Structure using C++	Mr. Manoj Kumar S B
F	561	55	54	51		Lab				18 ECL 57	DSP Lab	Mrs. Anusha M N /Ms. Lakshmi D L
S	53	54	561	52						18 ECL 58	HDL Lab	Mr. Manoj Kumar S B / Mrs. Rashmi S/ Divyashree R H

7th A						17 EC 71	Microwave and Antenna	Mr. Sandeep S V				
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	17 EC 72	Digital Image processing	Dr. Naveen B	
M	752	72	71	743		Lab				17 EC 73	Power Electronics	Mrs. Kavitha B C
T	71	743	73	72		Lab				17 EC 743	Real Time System	Mrs. Shalini K J
W	73	752	743	71		Lab				17 EC 752	IOT and Wireless Sensor Networks	Ms. Nischitha S
T	752	73	72	743						17 ECL 76	Advanced Communication Lab	Mrs. Nandini S / Ms. Mouna K M
F	72	71	752	73						17 ECL 77	VLSI Lab	Mr. Balaji B S / Mr. Sandeep S V
S										17 ECP 78	Project Work Phase-I + Project Seminar	Mr. Manoj Kumar S B, Mr. Goutham V

7th B						17 EC 71	Microwave and Antenna	Mr. Sandeep S V				
Days	I	II	III	IV	LUNCH BREAK	V	VI	VII	17 EC 72	Digital Image processing	Dr. Naveen B	
M	71	743	752	72						17 EC 73	Power Electronics	Mrs. Kavitha B C
T	73	72	71	743						17 EC 743	Real Time System	Mrs. Shalini K J
W	743	Lab				71	73	752		17 EC 752	IOT and Wireless Sensor Networks	Ms. Nischitha S
T	72	743	752	73		Lab				17 ECL 76	Advanced Communication Lab	Ms. Nischitha S / Ms. Mouna K M
F	752	73	72	71		Lab				17 ECL 77	VLSI Lab	Mrs. Shalini K J / Mr. Sandeep S V
S										17 ECP 78	Project Phase-I + Project Seminar	Mr. Manoj Kumar S B, Mr. Goutham V

**Signature of HOD**